



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ronald D. McCallister

Serial No.: 10/766,801

Group Art Unit: Unknown

Filed: 27 January 2004

Examiner: Unknown

For: A DISTORTION-MANAGED DIGITAL RF COMMUNICATIONS TRANSMITTER AND METHOD
THEREFOR

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. § 1.56, the references listed on the attached form PTO/SB/08B are being brought to the attention of the Examiner for consideration in connection with the examination of the above-identified patent application. Copies of the cited documents are enclosed.

This information is being submitted subsequent to the later of three months after the filing date of the present application but before the first mailing of the first Office Action on the merits.

It is respectfully requested that the Examiner indicate consideration of the cited references by returning a copy of the attached form PTO/SB/08B with initials or other appropriate marks.

Respectfully submitted,

Lowell W. Gresham

Attorney for Applicant

Registration No. 31,165

6 May 2004

MESCHKOW & GRESHAM, P.L.C.

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AND METHOD THEREFOR*

CERTIFICATE OF MAILING

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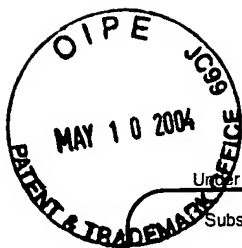
MESCHKOW & GRESHAM, P.L.C.
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Lowell W. Gresham
Signature

Respectfully submitted,

Lowell W. Gresham

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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of 2

Complete if Known

Application Number	10/766,801
Filing Date	27 January 2004
First Named Inventor	Ron McCallister
Art Unit	
Examiner Name	
Attorney Docket Number	2298-030

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 2003/0227981	12/11/2003	Vella-Coleiro, et al	
		US- 2003/0076894	04/24/2003	Jin, et al.	
		US- 2002/0180523	12/05/2002	Okubo, et al.	
		US- 2004/0021517	02/05/2004	Irvine, et al.	
		US- 2004/0013206	01/22/2004	Gamm, et al.	
		US- 2003/0020538	01/30/2003	Kim	
		US- 5,760,646	06/02/1998	Belcher, et al.	
		US- 6,118,335	09/12/2000	Nielsen, et al.	
		US- 5,923,712	07/13/1999	Leyendecker, et al.	
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	10/766,801
		Filing Date	27 January 2004
		First Named Inventor	Ron D. McCallister
		Art Unit	
		Examiner Name	
Sheet	2	of	2
		Attorney Docket Number	2298-030

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		GLENTIS, KOUKOULAS, & KALOUPSIDIS "Efficient Algorithms for Volterra System Identification" IEEE Transactions on Signal Processing, Vol 47, No. 11, Nov 1999.	
		DING, RAICH, & ZHOU "A Hammerstein Predistortion Linearization Design Based on the Indirect Learning Architecture" Georgia Inst. of Tech. School of Elect. & Comp. Engineering.	
		RAZZAGHI & CHANG "A 10-b, 1GSample/s ADC using SiGe BiCMOS Technology" Electrical Engineering Department, UCLA.	
		DANESHRAD "DSP VLSI Engine for Electronic Linearization of Fiber Optic Links" Wireless Integrated Systems Research Group, UCLA, 02/27/2001.	
		FOX "Analysis and Dynamic Range Enhancement of the Analog-to-Digital Interface in Multimode Radio Receivers" Virginia Polytechnic Inst. & State Univ. 02/1997.	
		HUMMELS "Linearization of ADCS and DACS for All-Digital Wide-Bandwidth Receivers" Depart. of Electrical & Comp. Engineering, Univ. of Maine, 09/09/1999.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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